

Notice of Allowability

Application No.

10/647,397

Examiner

Khanh Dang

Applicant(s)

HEWITT ET AL.

Art Unit

2111

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 2/20/2007 Amendment and 4/5/2007 Interview.
2. ☒ The allowed claim(s) is/are 26-31 and 33-35.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date 20070220
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☐ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____



Khanh Dang
Primary Examiner

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Zagorin on 4/5/2007.

The application has been amended as follows:

1. – 25. (Canceled)

26. (Currently amended) A method for configuring a point to point communication link coupling a first and a second device, the method comprising:

configuring a first communication link interface in the first device, the configuring including,

setting in a transmit width field for the link interface in the first device a transmit width of a transmit portion of the first communication link interface based on a lesser of a maximum transmit width of the transmit portion of the first communication link interface specified in a maximum transmit width field on the first device and a maximum receive width of a receive portion of a second communication link interface in the second device; and
setting in a receive width field for the link interface in the first device a receive width of a receive portion of the first communication link interface, separately from setting the transmit width, based on a lesser of a maximum receive width of the receive portion of the first communication link interface specified in a maximum receive width field for the link interface in the first device and a maximum transmit width of a transmit portion of the second communication link interface.

27. (Currently amended) The method as recited in claim 26 further comprising:

configuring the second communication link interface in the second device, the configuring including,

setting a transmit width of a transmit portion of the second communication link interface based on a lesser of a maximum transmit width of the transmit portion of the second communication link interface specified in a maximum transmit width field for the link interface in the second

device and the maximum receive width of the first communication link interface; and
setting a receive width of a receive portion of the second communication link interface separately from setting the transmit width based on a lesser of a maximum receive width of the receive portion of the second communication link interface specified in a maximum receive width field [[on]] for the link interface in the second device and the maximum transmit width of the first communication link interface.

28. (Currently amended) A method for configuring a first integrated circuit for communicating on a communication link having a separate transmit portion and a receive portion, the method comprising:

providing for the first integrated circuit a receive width field, a transmit width field, a maximum receive width field and a maximum transmit width field, the maximum transmit and receive width fields specifying a physical size of the separate transmit and receive portions, respectively, for the communication link;
setting the receive width field in the first integrated circuit to be the smaller of the maximum receive width field and a maximum transmit width field in a second communication interface on a second integrated circuit; and
setting the transmit width field to be the smaller of the maximum transmit width field and a second maximum receive width field in the second integrated circuit, thereby specifying the transmit and receive widths for the link.

29. (Previously presented) The method as recited in claim 28 further comprising setting a default width of the transmit width field and the receive width field.

30. (Previously presented) The method as recited in claim 1 wherein the default width is one byte.

31. (Currently amended) An integrated circuit comprising:
configuration registers for configuring a link interface for a communication link, the

from the transmit portion, the configuration registers including a receive width field, a maximum receive width field, a transmit width field, and a maximum transmit width field, and wherein the maximum receive width field provides a physical width of the ~~transmit~~ receive portion of the link on the integrated circuit, and wherein the maximum transmit width field provides a physical width of the transmit portion of the link on the integrated circuit; and wherein the receive width field specifies the receive width of the receive portion and the transmit width field specifies the transmit width of the transmit portion; and wherein the receive width field is programmed to be the smaller of the maximum receive width field and a second maximum transmit width field in a second integrated circuit coupled to the communication link; and wherein the transmit width field is programmed to be the smaller of the maximum transmit width field and a second maximum receive width field in the second integrated circuit, thereby specifying the transmit and receive widths for the communication link.

32. Canceled

33. (Previously presented) The integrated circuit as recited in claim 31 wherein the transmit and receive width fields are configured to a default value.

34. (Previously presented) The integrated circuit as recited in claim 33 wherein the default value is one bit.

35. (Previously presented) The integrated circuit as recited in claim 33 wherein the default value is one byte.



Khanh Dang
Primary Examiner